# Appendix for UniMiss 

Yutong Xie ${ }^{1[0000-0002-6644-1250]}$, Jianpeng Zhang ${ }^{2}$, Yong Xia ${ }^{2}$, and Qi Wu ${ }^{1 \star}$<br>${ }^{1}$ The University of Adelaide, Australia<br>${ }^{2}$ School of Computer Science and Engineering, Northwestern Polytechnical University, China<br>yutong.xie678@gmail.com;qi.wu01@adelaide.edu.au

## 1 Overview

In this document, we provide more discussions and experimental details to supplement the main submission. We first continue to discuss the necessity of switchable patch embedding (SPE) module (Section 2). We then give more details for the downstream tasks, including the implementation details and architectures (Section 3). Finally, we provide an intuitive explanation of the proposed volume-slice consistency mechanism (Section 4).

## 2 Necessity of SPE (Cont.)

To further explain the necessity of the SPE module, we compared the pyramid U-like medical Transformer (MiT) to two variants without a SPE module. For the variant 1 , we directly flatten the $2 \mathrm{D} / 3 \mathrm{D}$ images to a sequence based on the pixels/voxels level and then use a linear layer for the embedding. Such a crude flattening operation suffers the very high computation complexity and memory requirements, especially for 3 D images. Thus, it is hard to perform the variant 1 for quantitative comparisons. For the variant 2, we perform a naive embedding strategy to reduce the complexity. We first down-sample (for encoder)/up-sample (for decoder) the 2D/3D images by using a parameter free interpolation, then flatten them into a sequence based on the pixels/voxels level, and finally use a linear layer for both 2D and 3D embedding. The results in Table 1 show that MiT with the SPE module is significantly superior to the naive embedding strategy (i.e. variant 2) whenever with or without using the pre-training. It suggests that our SPE is better than the parameter-free interpolation and linear layer. The reason may be that the strided convolution with a large kernel is able to model the local continuity of 2D/3D images, which cannot be implemented by the linear layer.

## 3 Downstream Tasks

### 3.1 Implementation Details

In Table 2, we provide the implementation details of six downstream datasets, including the task type, modality, number of training and test cases, loss func-

[^0]Table 1. Segmentation performance of MiT and its two variants without a SPE module on BCV offline test set (3D CT).

| Methods |  | SPE | Dice |
| :---: | :---: | :---: | :---: |
| Random initialization | Variant 1 | No | unaffordable |
|  | Variant 2 | No | 73.31 |
|  | Ours | Yes | 79.93 |
| UniMiSS pre-training | Variant 1 | No | unaffordable |
|  | Variant 2 | No | 76.65 |
|  | Ours | Yes | 84.99 |

Table 2. Implementation details of downstream tasks. Seg: Segmentation; Cls: Classification; CE: Cross-entropy loss; off: offline test set; on: online test set.

| Dataset | BCV | RICORD | JSRT | ChestXR | CHAOS | ISIC |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Task | Seg | Cls | Seg | Cls | Seg | Seg |
| Modality | 3D CT | 3D CT | 2D X-ray | 2D X-ray | 3D MRI | 2D Dermoscopic |
| Training data | 24 | 182 | 124 | 17,955 | 16 | 2000 |
| Test data | 6 (off) +20 (on) | 45 | 123 | 3,430 | 4 | 600 |
| Loss | Dice+CE [4] | CE | Dice+CE | CE | Dice+CE [4] | Hybrid loss [6] |
| Patch size | $48 \times 192^{2}$ | $64 \times 128^{2}$ | $224^{2}$ | $224^{2}$ | $48 \times 192 \times 256$ | $224^{2}$ |
| Augmentation | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Optimizer | AdamW | AdamW | AdamW | AdamW | AdamW | AdamW |
| Learning rate | 0.0001 | 0.00001 | 0.0001 | 0.0001 | 0.0001 | 0.0001 |
| Batch size | 2 | 8 | 32 | 32 | 2 | 16 |
| Iterations | 25,000 | 14,000 | 10,000 | 17,000 | 50,000 | 37,500 |

tion, patch size, batch size, optimizer, learning rate, and maximum iterations. Note that we randomly split $25 \%$ training scans as a validation set to select the hyper-parameters of UniMiSS in the ablation study. We use the online data augmentation to alleviate the over-fitting of UniMiSS on training data. We augment 2D images via random cropping and zooming, random rotation, shear, shift, and horizontal/vertical flip. As for 3D images, we perform random rotation, scaling, flipping, adding white Gaussian noise, Gaussian blurring, adjusting rightness and contrast, simulation of low resolution, and Gamma transformation [4]. All the downstream experiments were performed on a NVIDIA GTX 2080Ti GPU.

### 3.2 Architectures of MiT and ResUnet

Figure 1 shows the detailed settings of the MiT network. The MiT encoder follows a progressive shrinking pyramid Transformer, as done in [5]. It consists explicitly of four stages, each of which is composed of a SPE module and several stacked Transformers. In each stage, the SPE module down-samples the input features and generates the dimension-specific embedded sequence. Notably, we append an extra learnable SSL token [1,2] to the patch embedded sequence. The SSL token is similar to the [CLS] token in ViT, which is able to aggregate information from the whole patch embedding tokens via the self-attention. The resultant sequences, combined with the learnable positional embedding, are inputted into the following Transformers for the long-term dependency model-
ing. Each Transformer layer includes a self-attention module and a feed-forward network (FFN) with two hidden layers. To reduce the computational cost and enable MiT to process high-resolution images, we follow the spatial-reduction attention (SRA) layer to reduce the spatial complexity [5]. MiT has a symmetric decoder structure that consists of three stages. In each stage, the input feature map is first up-sampled by the SPE module, and then refined by the stacked Transformer layers. Besides, we also add skip connections between the encoder and decoder to keep more low-level but high-resolution information. We devise two MiT by changing the number of Transformer layers, namely MiT-7 and MiT22. Noticed that default MiT-22 is used in the main submission unless otherwise specified.

Figure 2 shows the architecture of CNN-based ResUnet, used by the compared PCRL [7]. It consists of a 2D/3D ResNet-50 [3] encoder, a decoder, and four skip connections between encoder and decoder. The decoder contains five up-sampling modules. Each of the first four modules has a transposed convolutional (TransConv) layer followed by a convolution block (ConvBlock) and a pixel-wise summation with the corresponding feature maps from the encoder and the TransConv layer. The last module comprises an Up-sampling layer followed by a $1 \times 1$ Conv layer that maps each 32 -channel feature map to the desired number of classes.

## 4 Volume-slice consistency mechanism

Figure 3 gives an intuitive explanation of the proposed volume-slice consistency mechanism. Given a 3D volumetric image, we first create two augmented views via data augmentation, each of which has $m 2 \mathrm{D}$ slices. We then compute the volumetric or slice representations of dual paths, i.e. $\boldsymbol{f}_{1}^{\text {Volume }}, \boldsymbol{f}_{2}^{\text {Volume }}, \boldsymbol{f}_{1}^{\text {Slices }}$, and $\boldsymbol{f}_{2}^{\text {Slices }}$. Here the slice representations $\boldsymbol{f}_{1}^{\text {Slices }}$ and $\boldsymbol{f}_{2}^{\text {Slices }}$ are generated by averaging the outputs of $m$ slices. The loss function is composed of four items, including $\mathcal{L}^{\text {Volume }}, \mathcal{L}^{\text {Slices }}, \mathcal{L}^{\text {Volume } \rightarrow \text { Slices }}$, and $\mathcal{L}^{\text {Slices } \rightarrow \text { Volume }}$. The first two items aim to achieve the consistency at the level of global volume and local slices, respectively. Besides, the consistency across both levels should also be satisfied, which is achieved by the latter two items. By jointly using these four loss items, our model is able to capture richer representations from 3D medical images.

## References

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| Layer_name |  |  | MiT-7 |  | MiT-22 |  | Output Size |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Encoder | SPE |  | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{gathered} \hline \text { Kernel: } 7 \times 7 \\ \text { Channel: } 32 \\ \text { Stride: } 2 \end{gathered}$ | Kernel: $7 \times 7 \times 7$ <br> Channel: 32 <br> Stride: $(1,2,2)$ | $\begin{aligned} & \hline \text { Kernel: } 7 \times 7 \\ & \text { Channel: } 32 \\ & \text { Stride: } 2 \end{aligned}$ | $\begin{array}{\|c\|} \hline \text { Kernel: } 7 \times 7 \times 7 \\ \text { Channel: } 32 \\ \text { Stride: }(1,2,2) \end{array}$ | $\frac{\mathrm{H}}{2} \times \frac{\mathrm{w}}{2}$ | D $\times \frac{\mathrm{H}}{2} \times \frac{\mathrm{w}}{2}$ |
|  | Stage 1 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{gathered} \text { Kernel: } 3 \times 3 \\ \text { Channel: } 48 \\ \text { Stride: } 2 \end{gathered}$ | $\begin{array}{\|c\|} \hline \text { Kernel: } 3 \times 3 \times 3 \\ \text { Channel: } 48 \\ \text { Stride: } 2 \\ \hline \end{array}$ | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \\ \text { Channel: } 48 \\ \text { Stride: } 2 \end{gathered}$ | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \times 3 \\ \text { Channel: } 48 \\ \text { Stride: } 2 \end{gathered}$ | $\frac{\mathrm{H}}{4} \times \frac{\mathrm{w}}{4}$ | $\frac{\mathrm{D}}{2} \times \frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}$ |
|  |  | Transformer Layers | $\begin{array}{ll}  & \\ R=6 & \\ H=1 & \times 1 \\ E=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=6 & \\ \mathrm{H}=1 & \times 2 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}+1$ | $\frac{\mathrm{D}}{2} \times \frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}+1$ |
|  | Stage 2 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{gathered}$ | Kernel: $3 \times 3 \times 3$ <br> Channel: 128 <br> Stride: 2 | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{gathered}$ | Kernel: $3 \times 3 \times 3$ Channel: 128 Stride: 2 | $\frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}$ | $\frac{\mathrm{D}}{4} \times \frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline \mathrm{R}=4 & \\ \mathrm{H}=2 & \times 1 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=4 & \\ \mathrm{H}=2 & \times 3 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}+1$ | $\frac{\mathrm{D}}{4} \times \frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}+1$ |
|  | Stage 3 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \\ \text { Channel: } 256 \\ \text { Stride: } 2 \\ \hline \end{gathered}$ | Kernel: $3 \times 3 \times 3$ <br> Channel: 256 <br> Stride: 2 | Kernel: $3 \times 3$ <br> Channel: 256 <br> Stride: 2 | Kernel: $3 \times 3 \times 3$ <br> Channel: 256 <br> Stride: 2 | $\frac{\mathrm{H}}{16} \times \frac{\mathrm{w}}{16}$ | $\frac{\mathrm{D}}{8} \times \frac{\mathrm{H}}{16} \times \frac{\mathrm{w}}{16}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline \mathrm{R}=2 & \\ \mathrm{H}=4 & \times 1 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=2 & \\ \mathrm{H}=4 & \times 4 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{16} \times \frac{\mathrm{w}}{16}+1$ | $\begin{gathered} \frac{\mathrm{D}}{8} \times \frac{\mathrm{H}}{16} \times \frac{\mathrm{W}}{16}+ \\ 1 \end{gathered}$ |
|  | Stage 4 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{aligned} & \text { Kernel: } 3 \times 3 \\ & \text { Channel: } 512 \\ & \text { Stride: } 2 \end{aligned}$ | Kernel: $3 \times 3 \times 3$ <br> Channel: 512 <br> Stride: 2 | $\begin{gathered} \hline \text { Kernel: } 3 \times 3 \\ \text { Channel: } 512 \\ \text { Stride: } 2 \end{gathered}$ | Kernel: $3 \times 3 \times 3$ Channel: 512 Stride: 2 | $\frac{\mathrm{H}}{32} \times \frac{\mathrm{W}}{32}$ | $\frac{\mathrm{D}}{16} \times \frac{\mathrm{H}}{32} \times \frac{\mathrm{w}}{32}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline \mathrm{R}=1 & \\ \mathrm{H}=8 & \times 1 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=1 & \\ \mathrm{H}=8 & \times 3 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{32} \times \frac{\mathrm{w}}{32}+1$ | $\begin{gathered} \frac{\mathrm{D}}{16} \times \frac{\mathrm{H}}{32} \times \frac{\mathrm{w}}{32}+ \\ 1 \end{gathered}$ |
| Decoder | Stage 1 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | Kernel: $2 \times 2$ Channel: 256 Stride: 2 | Kernel: $2 \times 2 \times 2$ <br> Channel: 256 <br> Stride: 2 | Kernel: $2 \times 2$ <br> Channel: 256 <br> Stride: 2 | Kernel: $2 \times 2 \times 2$ <br> Channel: 256 <br> Stride: 2 | $\frac{\mathrm{H}}{16} \times \frac{\mathrm{W}}{16}$ | $\frac{\mathrm{D}}{8} \times \frac{\mathrm{H}}{16} \times \frac{\mathrm{W}}{16}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline \mathrm{R}=2 & \\ \mathrm{H}=8 & \times 1 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \mathrm{R}=2 & \\ \mathrm{H}=8 & \times 3 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{16} \times \frac{\mathrm{W}}{16}+1$ | $\begin{gathered} \frac{\mathrm{D}}{8} \times \frac{\mathrm{H}}{16} \times \frac{\mathrm{W}}{16}+ \\ 1 \end{gathered}$ |
|  | Stage 2 | SPE | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  |  | $\begin{gathered} \hline \text { Kernel: } 2 \times 2 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{gathered}$ | $\begin{array}{\|c\|} \hline \text { Kernel: } 2 \times 2 \times 2 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{array}$ | $\begin{gathered} \hline \text { Kernel: } 2 \times 2 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{gathered}$ | $\begin{array}{\|c\|} \hline \text { Kernel: } 2 \times 2 \times 2 \\ \text { Channel: } 128 \\ \text { Stride: } 2 \\ \hline \end{array}$ | $\frac{\mathrm{H}}{8} \times \frac{\mathrm{w}}{8}$ | $\frac{\mathrm{D}}{4} \times \frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline R=4 & \\ H=4 & \times 1 \\ E=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=4 & \\ \mathrm{H}=4 & \times 4 \end{array}$ |  | $\frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}+1$ | $\frac{\mathrm{D}}{4} \times \frac{\mathrm{H}}{8} \times \frac{\mathrm{W}}{8}+1$ |
|  | Stage 3 |  | 2D | 3D | 2D | 3D | 2D | 3D |
|  |  | SPE | Kernel: $2 \times 2$ <br> Channel: 48 <br> Stride: 2 | Kernel: $2 \times 2 \times 2$ <br> Channel: 48 <br> Stride: 2 | Kernel: $2 \times 2$ Channel: 48 Stride: 2 | Kernel: $2 \times 2 \times 2$ <br> Channel: 48 <br> Stride: 2 | $\frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}$ | $\frac{\mathrm{D}}{2} \times \frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}$ |
|  |  | Transformer Layers | $\begin{array}{ll} \hline \mathrm{R}=6 & \\ \mathrm{H}=2 & \times 1 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\begin{array}{ll} \hline \mathrm{R}=6 & \\ \mathrm{H}=2 & \times 3 \\ \mathrm{E}=4 & \\ \hline \end{array}$ |  | $\frac{\mathrm{H}}{4} \times \frac{\mathrm{w}}{4}+1$ | $\frac{\mathrm{D}}{2} \times \frac{\mathrm{H}}{4} \times \frac{\mathrm{W}}{4}+1$ |

Fig. 1. Detailed settings of MiT network. Here, 'R': reduction ratio of SRA; 'H': head number of SRA; and ' E ': expansion ratio of FFN
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Fig. 2. Detailed architecture of ResUnet: A 2D/3D ResNet-50 [3] encoder, a decoder, and four skip connections between encoder and decoder. Green 'ConvBlock': 2D ConvBatch Normalization(BN)-ReLU or 3D Conv-IN-LeakyReLU; Yellow 'TransConv': 2D/3D transposed convolutional layer. Note that the numbers in each block / layer indicate the number of filters, kernel size, and stride, respectively.


Fig. 3. Intuitive explanation of volume-slice consistency mechanism.
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[^0]:    * Corresponding author.

